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**MPS-G Gripper**  
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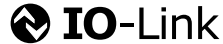
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Please note the validity of the additional operating instructions for automation functions

## 1. Device features

Supported Smart Sensor Profile Function Classes	none (Smart Sensor Profile not supported)
Supported IO-Link Time Stamp Profile modes	none (IO-Link Time Stamp Profile not supported)
Block Parameter Transmission	not supported
Data Storage functionality	supported
Access Locks (supported / modes)	Data Storage , Local User Interface

## 2. Physical layer

Note: The IO-Link Device's max. current consumption (inclusive load current) shall not exceed the master port's max. output power current.

SIO Modus	yes
Min Cycle Time	1.0 ms
Baudrate <sup>2</sup>	COM3
Process Data Length (IN)	4 Byte
IODD version	V3.01
Valid for IO-Link version	1.1.0

## 3. Process data

Record: 4 Byte

Bitoffset																																																																								
Byte 0	Measurement value	31	30	29	28	27	26	25	24																																																															
Type/Subindex	Integer 16																																																																							
Bitoffset																																																																								
Byte 1	Measurement value	23	22	21	20	19	18	17	16																																																															
Type/Subindex	Integer 16																																																																							
Bitoffset																																																																								
Byte 2	Scale	15	14	13	12	11	10	9	8																																																															
Type/Subindex	Integer 8																																																																							
Bitoffset																																																																								
Byte 3	Qint.X / Alert <1>	7	6	5	4	3	2	1	0	Qint.X / Alert <2>	7	6	5	4	3	2	1	0	Qint.X / Alert <3>	7	6	5	4	3	2	1	0	Qint.X / Alert <4>	7	6	5	4	3	2	1	0	Qint.X / Alert <5>	7	6	5	4	3	2	1	0	Qint.X / Alert <6>	7	6	5	4	3	2	1	0	Qint.X / Alert <7>	7	6	5	4	3	2	1	0	Qint.X / Alert <8>	7	6	5	4	3	2	1	0
Type/Subindex	Boolean	3	Boolean	4	Boolean	5	Boolean	6	Boolean	7	Boolean	8	Boolean	9	Boolean	10																																																								

## 4. Service data

The following ISDUs will not be saved via Data-Storage: Teach-in Channel, Device Specific Name, Find Me, Total actuator travel [sum m] and Cycle count [sum]

IO-Link specific	Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
	12 (0x0C)	Device Access Locks	Record	2 Byte	rw	The access to the device parameters can be restricted by setting appropriate flags within this parameter.		
	1 (0x01)	Parameter Write Access	Bit (0)	1 Bit	rw	true = Locked false = Unlocked	This lock prevents the write access to all read/write parameters of the device except for the parameter 'Device Access Locks'.	
	2 (0x02)	Data Storage	Bit (1)	1 Bit	rw	true = Locked false = Unlocked	This lock prevents the write access to the device parameters via the data storage mechanism.	
	3 (0x03)	Local Parameterization	Bit (2)	1 Bit	rw	true = Locked false = Unlocked	This lock prevents the device settings from being changed via local operating elements on the device.	
	4 (0x04)	Local User Interface	Bit (3)	1 Bit	rw	true = Locked false = Unlocked	This lock prevents the access to the device settings and display via a local user interface. The user interface is disabled.	
	16 (0x10)	Vendor Name	String	64 Byte	ro	SICK AG	The vendor name that is assigned to a Vendor ID.	
	17 (0x11)	Vendor Text	String	64 Byte	ro	<a href="http://www.sick.com">www.sick.com</a>	Additional information about the vendor.	
	18 (0x12)	Product Name	String	64 Byte	ro	MPS-G	Complete product name.	
	19 (0x13)	Product ID	String	64 Byte	ro	Vendor-specific product or type identification (e.g., item number or model number).		
	20 (0x14)	Product Text	String	64 Byte	ro	Additional product information for the device.		
	21 (0x15)	Serial Number	String	16 Byte	ro	Unique, vendor-specific identifier of the individual device.		
	22 (0x16)	Hardware Revision	String	64 Byte	ro	Unique, vendor-specific identifier of the hardware revision of the individual device.		
	23 (0x17)	Firmware Revision	String	64 Byte	ro	Unique, vendor-specific identifier of the firmware revision of the individual device.		
	24 (0x18)	Application-specific Tag	String	32 Byte	rw	***	Possibility to mark a device with user- or application-specific information.	
	36 (0x24)	Device Status	UInt	8 Bit	ro	0 = Device is OK 1 = Maintenance required 2 = Out of specification 3 = Functional check 4 = Failure	Indicator for the current device condition and diagnosis state.	

<sup>1</sup> ro = read only, wo = write only, rw = read/write  
<sup>2</sup> COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

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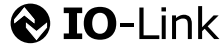
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### ENGLISH

IO-Link specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
37 (0x25)	Detailed Device Status	Array	192 Byte	ro	Octet String [64]	List of all currently pending events in the device.	
40 (0x28)	PD Input	PD In	4 Byte	ro		Last valid process input data of the device.	
SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
13 (0x0D)	Profile Characteristic	Record	6 Byte	ro			
1 (0x01)	Profile Identifier	Bit (32)	16 Bit	ro	10		
2 (0x02)	Profile Identifier	Bit (16)	16 Bit	ro	49		
3 (0x03)	Profile Identifier	Bit (0)	16 Bit	ro	16384		
14 (0x0E)	PDInput descriptor	Array	3 Byte	ro	Octet String [1]		
15 (0x0F)	PDOOutput descriptor	Array	3 Byte	ro	Octet String [1]		
25 (0x19)	Function Tag	String	32 Byte	rw	***	Function Tag	
26 (0x1A)	Location Tag	String	32 Byte	rw	***	Location Tag	
58 (0x3A)	Teach-in Channel	UInt	8 Bit	rw	0	0 = Default Quint (Quint. 1) 1 = Quint. 1 2 = Quint. 2 3 = Quint. 3 4 = Quint. 4 5 = Quint. 5 6 = Quint. 6 7 = Quint. 7 8 = Quint. 8	The Quint channel that is going to be teachd
59 (0x3B)	Teach-in Result	Record	1 Byte	ro		Teach-in result provides feedback on the status and the results of the teach-in activities	
1 (0x01)	State	Bit (0)	4 Bit	ro	0	0 = IDLE 1 = SP1 SUCCESS 2 = SP2 SUCCESS 3 = SP12 SUCCESS 4 = WAIT FOR COMMAND 5 = BUSY 7 = ERROR	
2 (0x02)	Flag SP1 TP1	Bit (4)	1 Bit	ro	false	true false	
3 (0x03)	Flag SP1 TP2	Bit (5)	1 Bit	ro	false	true false	
4 (0x04)	Flag SP2 TP1	Bit (6)	1 Bit	ro	false	true false	
5 (0x05)	Flag SP2 TP2	Bit (7)	1 Bit	ro	false	true false	
60 (0x3C)	Quint.1 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
61 (0x3D)	Quint.1 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 130 = Move 131 = Grip	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70	[x10µm]	
62 (0x3E)	Quint.2 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
63 (0x3F)	Quint.2 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 130 = Move 131 = Grip	

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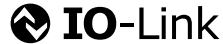
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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70	[x10µm]	
64 (0x40)	Device Specific Name	String	32 Byte	rw	***		
67 (0x43)	Process data user definition	Record	8 Byte	rw	Define the contents of the process data		
1 (0x01)	Bit 0	Bit (56)	8 Bit	rw	1	0 = Set bit to constant 0 1 = Qint.1 2 = Qint.2 3 = Qint.3 4 = Qint.4 5 = Qint.5 6 = Qint.6 7 = Qint.7 8 = Qint.8 60 = Group alert: Cycle time 65 = Direct alert: Operating hours max. 68 = Direct alert: Power cycles max. 69 = Direct alert: Cycle count max. 90 = Direct alert: Total actuator travel max.	Content of process data bit 0
2 (0x02)	Bit 1	Bit (48)	8 Bit	rw	2	Compare value range in subindex 1	Content of process data bit 1
3 (0x03)	Bit 2	Bit (40)	8 Bit	rw	3	Compare value range in subindex 1	Content of process data bit 2
4 (0x04)	Bit 3	Bit (32)	8 Bit	rw	4	Compare value range in subindex 1	Content of process data bit 3
5 (0x05)	Bit 4	Bit (24)	8 Bit	rw	5	Compare value range in subindex 1	Content of process data bit 4
6 (0x06)	Bit 5	Bit (16)	8 Bit	rw	6	Compare value range in subindex 1	Content of process data bit 5
7 (0x07)	Bit 6	Bit (8)	8 Bit	rw	7	Compare value range in subindex 1	Content of process data bit 6
8 (0x08)	Bit 7	Bit (0)	8 Bit	rw	8	Compare value range in subindex 1	Content of process data bit 7
120 (0x78)	Process data select	UInt	8 Bit	rw	0	0 = Measurement value (2 Bytes) + Scale (1 Byte) + 8xQints/Alerts (1 Byte)	
121 (0x79)	Pin 2 configuration	UInt	8 Bit	rw	36	0 = Deactivated 36 = Qint.2	
170 (0xAA)	Switchpoint width [x10µm]	Record	16 Byte	rw	Switchpoint width		
1 (0x01)	Qint.1 width	Bit (112)	16 Bit	rw	200	0...1000	[x10µm]
2 (0x02)	Qint.2 width	Bit (96)	16 Bit	rw	200	0...1000	[x10µm]
3 (0x03)	Qint.3 width	Bit (80)	16 Bit	rw	200	0...1000	[x10µm]
4 (0x04)	Qint.4 width	Bit (64)	16 Bit	rw	200	0...1000	[x10µm]
5 (0x05)	Qint.5 width	Bit (48)	16 Bit	rw	200	0...1000	[x10µm]
6 (0x06)	Qint.6 width	Bit (32)	16 Bit	rw	200	0...1000	[x10µm]
7 (0x07)	Qint.7 width	Bit (16)	16 Bit	rw	200	0...1000	[x10µm]
8 (0x08)	Qint.8 width	Bit (0)	16 Bit	rw	200	0...1000	[x10µm]
171 (0xAB)	Switchpoint tolerance [x10µm]	Record	6 Byte	rw	Switchpoint tolerance		
1 (0x01)	Qint.1 tolerance	Bit (32)	16 Bit	rw	100	[x10µm]	
2 (0x02)	Qint.2 tolerance	Bit (16)	16 Bit	rw	100	[x10µm]	
3 (0x03)	Qint.3 tolerance	Bit (0)	16 Bit	rw	100	[x10µm]	
204 (0xCC)	Find Me	UInt	8 Bit	rw	0	0 = Stop FindMe 1 = LED flash	
256 (0x100)	Position [x10µm]	Int	16 Bit	ro	Current position [x10µm]		
257 (0x101)	Position offset [x10µm]	Int	16 Bit	rw	0	-27500...27500	Offset that is added to the current position [x10µm]
260 (0x104)	Detection range [x10µm]	Record	4 Byte	rw			
1 (0x01)	Minimal position	Bit (16)	16 Bit	rw	-2500	-30000...30000	[x10µm]
2 (0x02)	Maximal position	Bit (0)	16 Bit	rw	2500	-30000...30000	[x10µm]
261 (0x105)	Current position repeatability [mm]	Float	4 Byte	ro	[mm]		
265 (0x109)	Position noise limit for application range [mm]	Float	4 Byte	rw	0.3	[mm]	
4356 (0x1104)	Operating hours	Record	12 Byte	ro	Operating Hours data		
1 (0x01)	Total	Bit (64)	32 Bit	ro	[h]		
2 (0x02)	Since last reset	Bit (32)	32 Bit	ro	[h]		
3 (0x03)	Since startup	Bit (0)	32 Bit	ro	[h]		
4357 (0x1105)	Power cycles	Record	8 Byte	ro	Power Cycles data		
1 (0x01)	Total	Bit (32)	32 Bit	ro			
2 (0x02)	Since last reset	Bit (0)	32 Bit	ro			

<sup>1</sup> ro = read only, wo = write only, rw = read/write  
<sup>2</sup> COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)



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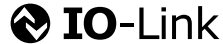
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Please note the validity of the additional operating instructions for automation functions

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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
4367 (0x110F)	DD - Reset device diagnostics parameters	UInt	8 Bit	wo	1 = Reset operating hours 2 = Reset power cycles 255 = Reset all device diagnostics parameters	DD - reset the device diagnostics parameters	
4368 (0x1110)	DD - Alert source	Record	2 Byte	rw	Set the alert source of the device diagnosis data		
1 (0x01)	Operating hours alert source	Bit (8)	8 Bit	rw	0	0 = Total 1 = Since Last Reset 2 = Since Startup	
2 (0x02)	Power cycles alert source	Bit (0)	8 Bit	rw	0	0 = Total 1 = Since Last Reset	
4369 (0x1111)	DD - Alert limit	Record	8 Byte	rw	Set the limits of the device diagnosis alerts		
1 (0x01)	Operating hours limit	Bit (32)	32 Bit	rw	876000	0..876000	[h]
2 (0x02)	Power cycles limit	Bit (0)	32 Bit	rw	1000000		
4370 (0x1112)	DD - Alert flags	Record	1 Byte	ro	Get the device diagnosis alerts		
1 (0x01)	Operating hours alert	Bit (1)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
2 (0x02)	Power cycles alert	Bit (0)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
4372 (0x1114)	Actuator travel [x10µm]	UInt	16 Bit	ro	[x10µm]		
4374 (0x1116)	Total actuator travel [sum m]	UInt	32 Bit	rw	[m]		
4375 (0x1117)	Average actuator velocity [m/s]	Record	8 Byte	ro			
1 (0x01)	Extend (positive direction)	Bit (32)	4 Byte	ro	Format = Float [m/s]		
2 (0x02)	Retract (negative direction)	Bit (0)	4 Byte	ro	Format = Float [m/s]		
4379 (0x111B)	Actuator travel time [ms]	Record	8 Byte	ro			
1 (0x01)	Extend (positive direction)	Bit (32)	4 Byte	ro	Format = Float [ms]		
2 (0x02)	Retract (negative direction)	Bit (0)	4 Byte	ro	Format = Float [ms]		
4380 (0x111C)	Cycle time [ms]	Float	4 Byte	ro	[ms]		
4381 (0x111D)	Dwell time [ms]	Record	8 Byte	ro			
1 (0x01)	Start position	Bit (32)	4 Byte	ro	Format = Float [ms]		
2 (0x02)	Stop position	Bit (0)	4 Byte	ro	Format = Float [ms]		
4382 (0x111E)	Cycle count [sum]	UInt	32 Bit	rw			
4398 (0x112E)	Reset actuator diagnostics parameters	UInt	8 Bit	wo	1 = Reset total actuator travel 2 = Reset actuator cycles 255 = Reset all actuator diagnostics parameters	DD - reset the device diagnostics parameters	
4399 (0x112F)	Actuator alert limits	Record	16 Byte	rw	Set the limits of the actuator alerts		
1 (0x01)	Total actuator travel limit	Bit (96)	32 Bit	rw	100000	[m]	
2 (0x02)	Min. cycle time limit	Bit (64)	4 Byte	rw	0	Format = Float [ms]	
3 (0x03)	Max. cycle time limit	Bit (32)	4 Byte	rw	86400000	Format = Float [ms]	
4 (0x04)	Cycle count limit	Bit (0)	32 Bit	rw	1000000		
4400 (0x1130)	Actuator alerts	Record	2 Byte	ro	Get the actuator alerts		
1 (0x01)	Total actuator travel alert	Bit (10)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
2 (0x02)	Min. cycle time alert	Bit (9)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
3 (0x03)	Max. cycle time alert	Bit (8)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
4 (0x04)	Cycle count alert	Bit (7)	1 Bit	ro	true = Alert occurred false = Alert did not occur		
4602 (0x11FA)	Current field strength [mT]	Record	8 Byte	ro	Magnetic field strength of magnetic elements		
1 (0x01)	Current1	Bit (32)	4 Byte	ro	Magnetic field strength of magnetic element 1. Format = Float [mT]		
2 (0x02)	Current2	Bit (0)	4 Byte	ro	Magnetic field strength of magnetic element 2. Format = Float [mT]		
4604 (0x11FC)	Peak field strength [mT]	Record	8 Byte	ro			
1 (0x01)	Current1	Bit (32)	4 Byte	ro	Magnetic field strength of magnetic element 1. Format = Float [mT]		
2 (0x02)	Current2	Bit (0)	4 Byte	ro	Magnetic field strength of magnetic element 2. Format = Float [mT]		
4842 (0x12EA)	Alert settings	Record	8 Byte	rw			

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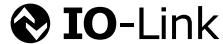
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Please note the validity of the additional operating instructions for automation functions

## ENGLISH

SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
1 (0x01)	Alert delay time [ms]	Bit (32)	32 Bit	rw	0	0...1000000 = Alert delay time between 0 and 1000s	Time for which alerts bits are delayed. An alert is only generated if the corresponding alert condition is fulfilled for longer than the alert delay time defined here. [ms]
2 (0x02)	Automatic alert reset time [ms]	Bit (0)	32 Bit	rw	-1	-1...1000000 = Automatic alert reset time between 0 and 1000s, -1 deactivates the automatic alert reset.	Time after which alert bits are reset automatically if the corresponding alert condition is no longer fulfilled. A negative value deactivates the automatic alert reset. [ms]
14905 (0x3A39)	Platform version	String	32 Byte	ro			
14908 (0x3A3C)	Sensor head firmware version	String	32 Byte	ro			
16384 (0x4000)	Qint.3 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0		Setpoint 1
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0		Setpoint 2
16385 (0x4001)	Qint.3 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0		0 = Not inverted 1 = Inverted
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0		0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 131 = Grip
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		[x10µm]
16386 (0x4002)	Qint.4 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0		Setpoint 1
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0		Setpoint 2
16387 (0x4003)	Qint.4 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0		0 = Not inverted 1 = Inverted
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0		0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		[x10µm]
16388 (0x4004)	Qint.5 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0		Setpoint 1
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0		Setpoint 2
16389 (0x4005)	Qint.5 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0		0 = Not inverted 1 = Inverted
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0		0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		[x10µm]
16390 (0x4006)	Qint.6 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0		Setpoint 1
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0		Setpoint 2
16391 (0x4007)	Qint.6 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0		0 = Not inverted 1 = Inverted
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0		0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		[x10µm]
16392 (0x4008)	Qint.7 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0		Setpoint 1
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0		Setpoint 2

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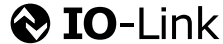
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SICK device specific							
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16393 (0x4009)	Qint.7 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70	[x10µm]	
16394 (0x400A)	Qint.8 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16395 (0x400B)	Qint.8 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70	[x10µm]	
16512 (0x4080)	MDC Descr	Record	11 Byte	ro			
1 (0x01)	Lower Limit	Bit (56)	32 Bit	ro	-2500	-2147482880...2147482880 = Permissible values for the Detection range	Lower value measurement range
2 (0x02)	Upper Limit	Bit (24)	32 Bit	ro	2500	-2147482880...2147482880 = Permissible values for the Detection range	Upper value measurement range
3 (0x03)	Unit code	Bit (8)	16 Bit	ro	1010	1010 = Distance [m]	
4 (0x04)	Scale	Bit (0)	8 Bit	ro	-5	-128...127 = Range shifting (10°scale)	
17341 (0x43BD)	FW Password	String	16 Byte	wo			
17342 (0x43BE)	HW ID Key	String	32 Byte	ro			
17343 (0x43BF)	Bootmode Status	UInt	8 Bit	ro		0 = Bootloader is inactive 1 = Bootloader is active	
Standard command							
Index dec (hex)	Name	Access <sup>1</sup>	Value	Name	Remark [Unit]		
2 (0x02)	System Command	wo	65	SP1 Single Value Teach			
			66	SP2 Single Value Teach			
			75	Dynamic Teach Start			
			76	Dynamic Teach Stop			
			77	Dynamic Teach Apply			
			79	Teach Cancel			
			80	Start unlocking sequence			
			81	Unlocking command 1			
			82	Unlocking command 2			
			128	Device Reset			Performs a system restart. All read-only parameter are rejected
			129	Application Reset			Reset all application specific values except the identification parameters
			130	Restore Factory Settings			All default parameter are reloaded
			192	Reset trained algorithm parameter			Reset for all actuator specific values related to position calculation of MPS-G
			228	Reset diagnostics parameter			Resets operating hours, power cycles, cycle count and total actuator travel
			229	Reset all present alerts			