



8021618 0717  
9269378 0717  
**WLG16 A00 BTLE**  
8389042  
3561899607  
9269377 0717 (1.1.0)

|  |  |
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Please note the validity of the additional operating instructions for automation functions

## 1. Physical layer

Note: The IO-Link Device's max. current consumption (inclusive load current) shall not exceed the the master port's max. output power current.

|                           |          |
|---------------------------|----------|
| SIO Modus                 | yes      |
| Min Cycle Time            | 2.3 ms   |
| Baudrate <sup>2</sup>     | COM2     |
| Process Data Length       | 2 Byte   |
| IODD version              | V1.11311 |
| Valid for IO-Link version | 1.1.0    |

## 2. Process data

Record<sup>3</sup>: 2 Byte

|               |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  |         |   |         |   |
|---------------|----|----|----|----|----|----|---|---|------|---|------|---|--|--|--|---------|---|---------|---|
| Bitoffset     |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  |         |   |         |   |
| Byte 0        | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 |      |   |      |   |  |  |  |         |   |         |   |
| Type/Subindex |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  |         |   |         |   |
| Bitoffset     |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  |         |   |         |   |
| Byte 1        | 7  | 6  | 5  | 4  | 3  | 2  | 1 | 0 | Bit1 | 1 | Bit0 | 0 |  |  |  |         |   |         |   |
| Type/Subindex |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  |         |   |         |   |
|               |    |    |    |    |    |    |   |   |      |   |      |   |  |  |  | Boolean | 2 | Boolean | 1 |

## 3. Service data

The following ISDUs will not be saved via Data-Storage: Profile characteristic, Teach-in channel, Teach-in state, Device / function name, Sender configuration, Quality of teach, Quality of run, Current receiver level (live), Upper threshold (switch-on) dynamic, Lower threshold (switch-off) dynamic, Find me and SLTI Version

| IO-Link specific |                               |                     |         |                     |               |   |               |
|------------------|-------------------------------|---------------------|---------|---------------------|---------------|---|---------------|
| Index dec (hex)  | Name                          | Format (Offset)     | Length  | Access <sup>1</sup> | Default Value | Value / Range   | Remark [Unit] |
| 12 (0x0C)        | Device Access Locks           | Record <sup>3</sup> | 2 Byte  | rw                  |               |   |               |
| 1 (0x01)         | Parameter (write) Access Lock | Bit (0)             | 1 Bit   | rw                  |               |   |               |
| 2 (0x02)         | Data Storage Lock             | Bit (1)             | 1 Bit   | rw                  |               |   |               |
| 3 (0x03)         | Local Parameterization Lock   | Bit (2)             | 1 Bit   | rw                  |               |   |               |
| 4 (0x04)         | Local User Interface Lock     | Bit (3)             | 1 Bit   | rw                  |               |   |               |
| 16 (0x10)        | Vendor Name                   | String              | 7 Byte  | ro                  | SICK AG       |   |               |
| 18 (0x12)        | Product Name                  | String              | 18 Byte | ro                  |               |   |               |
| 19 (0x13)        | Product ID                    | String              | 13 Byte | ro                  |               |   |               |
| 20 (0x14)        | Product Text                  | String              | 45 Byte | ro                  |               |   |               |
| 21 (0x15)        | Serial Number                 | String              | 8 Byte  | ro                  |               |   |               |
| 22 (0x16)        | Hardware Version              | String              | 4 Byte  | ro                  |               |   |               |
| 23 (0x17)        | Firmware Version              | String              | 19 Byte | ro                  |               |   |               |
| 24 (0x18)        | Application Specific Tag      | String              | 32 Byte | rw                  | *****         |   |               |
| 36 (0x24)        | Device Status                 | UInt                | 8 Bit   | ro                  |               | 0 = Device is OK<br>1 = Maintenance required<br>2 = Out of specification<br>3 = Functional check<br>4 = Failure<br>5...255 = Reserved |               |
| 40 (0x28)        | Process Data Input            | PD In               | 2 Byte  | ro                  |               |   |               |

<sup>1</sup> ro = read only, wo = write only, rw = read/write

<sup>2</sup> COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

<sup>3</sup>Subindex access not supported



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|----------------------|----------------------------------|---------------------|---------|---------------------|---------------|---|---------------|
| Index dec (hex)      | Name                             | Format (Offset)     | Length  | Access <sup>1</sup> | Default Value | Value / Range   | Remark [Unit] |
| 13 (0x0D)            | Profile characteristic           | Array <sup>3</sup>  | 10 Byte | ro                  |               | Unsigned Integer16 [5]  |               |
| 14 (0x0E)            | PDInput descriptor               | Array <sup>3</sup>  | 6 Byte  | ro                  |               | Octet String [2]  |               |
| 15 (0x0F)            | PDOutput descriptor              | Array <sup>3</sup>  | 3 Byte  | ro                  |               | Octet String [1]  |               |
| 58 (0x3A)            | Teach-in channel                 | UInt                | 8 Bit   | rw                  | 0             | 0...2 = Default BDC   |               |
| 59 (0x3B)            | Teach-in state                   | Record <sup>3</sup> | 1 Byte  | ro                  |               |   |               |
| 1 (0x01)             | Teach flags                      | Bit (4)             | 1 Bit   | ro                  |               | false = Teachpoint 1 not taught<br>true = Teachpoint 1 successfully taught  |               |
| 2 (0x02)             | Teach state                      | Bit (0)             | 4 Bit   | ro                  |               | 0 = IDLE<br>1 = SP1 SUCCESS<br>5 = BUSY   |               |
| 60 (0x3C)            | Qint.1 SP1 / SP2                 | Record <sup>3</sup> | 2 Byte  | rw                  |               |   |               |
| 1 (0x01)             | SP1 upper threshold (switch-on)  | Bit (8)             | 8 Bit   | rw                  | 90            | 10...90<br>110...200  | [%]           |
| 2 (0x02)             | SP2 lower threshold (switch-off) | Bit (0)             | 8 Bit   | rw                  | 85            | 5...85<br>105...195   | [%]           |
| 61 (0x3D)            | Qint.1 configuration             | Record <sup>3</sup> | 4 Byte  | rw                  |               |   |               |
| 1 (0x01)             | Switchpoint logic                | Bit (24)            | 8 Bit   | rw                  | 128           | 128 = Vendor specific   |               |
| 2 (0x02)             | Switchpoint mode                 | Bit (16)            | 8 Bit   | rw                  | 128           | 128 = Vendor specific   |               |
| 3 (0x03)             | Switchpoint hysteresis           | Bit (0)             | 16 Bit  | rw                  | 0             | 0 = Autodefined hysteresis  |               |
| 62 (0x3E)            | Qint.2 SP1 / SP2                 | Record <sup>3</sup> | 2 Byte  | rw                  |               |   |               |
| 1 (0x01)             | SP1 upper threshold (switch-on)  | Bit (8)             | 8 Bit   | rw                  | 90            | 10...90<br>110...200  |               |
| 2 (0x02)             | SP2 lower threshold (switch-off) | Bit (0)             | 8 Bit   | rw                  | 85            | 5...85<br>105...195   |               |
| 63 (0x3F)            | Qint.2 configuration             | Record <sup>3</sup> | 4 Byte  | rw                  |               |   |               |
| 1 (0x01)             | Switchpoint logic                | Bit (24)            | 8 Bit   | rw                  | 128           | 128 = Vendor specific   |               |
| 2 (0x02)             | Switchpoint mode                 | Bit (16)            | 8 Bit   | rw                  | 128           | 128 = Vendor specific   |               |
| 3 (0x03)             | Switchpoint hysteresis           | Bit (0)             | 16 Bit  | rw                  | 0             | 0 = Autodefined hysteresis  |               |
| 64 (0x40)            | Device / function name           | String              | 32 Byte | rw                  | *****         |   |               |
| 83 (0x53)            | Detection mode                   | UInt                | 8 Bit   | rw                  | 0             | 0 = Highly-transparent objects<br>1 = Semi-transparent objects<br>2 = Opaque objects<br>3 = Bottles / trays<br>4 = Foil tear<br>255 = Manual  |               |
| 97 (0x61)            | Sender configuration             | UInt                | 8 Bit   | rw                  | 0             | 0 = Sender active<br>1 = Sender not active  |               |
| 112 (0x70)           | AutoAdapt                        | UInt                | 8 Bit   | rw                  | 1             | 0 = Off<br>1 = On - time based<br>2 = On - event based  |               |
| 114 (0x72)           | Quality of teach                 | UInt                | 8 Bit   | ro                  |               | 0...100   | [%]           |
| 121 (0x79)           | Pin 2 configuration              | UInt                | 8 Bit   | rw                  |               | 0 = Deactivated / no function<br>1 = External input<br>16 = Sender off<br>17 = Teach-in<br>32 = Detection output Q/<br>33 = Quality of run alarm output<br>34 = Switching signal QL2<br>35 = Detection output QInt.1<br>39 = Switching signal QL1 |               |

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| Index dec (hex)      | Name                                 | Format (Offset)     | Length  | Access <sup>1</sup> | Default Value | Value / Range   | Remark [Unit] |
| 175 (0xAF)           | Quality of run                       | UInt                | 8 Bit   | ro                  |               | 0...255   | [%]           |
| 176 (0xB0)           | Quality of run alarm threshold       | UInt                | 8 Bit   | rw                  | 50            | 0...90  | [%]           |
| 177 (0xB1)           | Quality of alignment                 | UInt                | 8 Bit   | ro                  |               | 0...255   | [%]           |
| 180 (0xB4)           | Current receiver level (live)        | UInt                | 8 Bit   | ro                  |               | 0...255   | [%]           |
| 181 (0xB5)           | Upper threshold (switch-on) dynamic  | UInt                | 8 Bit   | ro                  |               | 0...255   | [%]           |
| 182 (0xB6)           | Lower threshold (switch-off) dynamic | UInt                | 8 Bit   | ro                  |               | 0...255   | [%]           |
| 204 (0xCC)           | Find me                              | UInt                | 8 Bit   | rw                  | 0             | 0 = Find me deactivated<br>1 = Find me activated  |               |
| 205 (0xCD)           | SICK profile version                 | String              | 4 Byte  | ro                  |               |   |               |
| 219 (0xDB)           | Article No.                          | Record              | 7 Byte  | ro                  |               |   |               |
| 1 (0x01)             | Article No. IO-Link Device           | Bit (0)             | 7 Byte  | ro                  |               |   |               |
| 226 (0xE2)           | System state                         | Record <sup>3</sup> | 1 Byte  | ro                  |               |   |               |
| 1 (0x01)             | Quality of run alarm output          | Bit (1)             | 1 Bit   | ro                  |               | false = Alarm not active<br>true = Alarm active   |               |
| 2 (0x02)             | Detection output Q.int               | Bit (0)             | 1 Bit   | ro                  |               | false = No object detected<br>true = Object detected  |               |
| 227 (0xE3)           | Notification handling                | UInt                | 8 Bit   | rw                  | 0             | 0 = All enabled<br>1 = All disabled<br>2 = Events enabled, PD invalid flag disabled<br>3 = Events disabled, PD invalid flag enabled |               |
| 230 (0xE6)           | Bluetooth                            | UInt                | 8 Bit   | rw                  | 1             | 0 = Bluetooth deactivated<br>1 = Bluetooth activated  |               |
| 231 (0xE7)           | MAC address filter                   | Record <sup>3</sup> | 49 Byte | rw                  |               |   |               |
| 1 (0x01)             | Status                               | Bit (384)           | 1 Bit   | rw                  | 0             | false = MAC address filter deactivated<br>true = MAC address filter activated   |               |
| 2 (0x02)             | MAC address 1                        | Bit (288)           | 12 Byte | rw                  |               |   |               |
| 3 (0x03)             | MAC address 2                        | Bit (192)           | 12 Byte | rw                  |               |   |               |
| 4 (0x04)             | MAC address 3                        | Bit (96)            | 12 Byte | rw                  |               |   |               |
| 5 (0x05)             | MAC address 4                        | Bit (0)             | 12 Byte | rw                  |               |   |               |
| 232 (0xE8)           | Passkey                              | Record <sup>3</sup> | 7 Byte  | rw                  |               |   |               |
| 1 (0x01)             | Status                               | Bit (48)            | 1 Bit   | rw                  | 0             | false = Passkey deactivated<br>true = Passkey activated   |               |
| 2 (0x02)             | Key                                  | Bit (0)             | 6 Byte  | rw                  |               |   |               |
| 1080 (0x438)         | SLTI Version                         | String              | 8 Byte  | ro                  |               |   |               |
| 1083 (0x43B)         | Logic 1                              | UInt                | 8 Bit   | rw                  |               | 0 = DIRECT<br>1 = AND<br>2 = OR<br>3 = Window Mode<br>4 = Hysteresis  |               |
| 1084 (0x43C)         | Logic 2                              | UInt                | 8 Bit   | rw                  |               | 0 = DIRECT<br>1 = AND<br>2 = OR<br>3 = Window Mode<br>4 = Hysteresis  |               |
| 1085 (0x43D)         | Timer 1 Mode                         | UInt                | 8 Bit   | rw                  | 0             | 0 = deactivated<br>1 = T-on delay<br>2 = T-off delay<br>3 = T-on/T-off delay<br>4 = Impulse (one shot)                              |               |

<sup>1</sup>ro = read only, wo = write only, rw = read/write

<sup>2</sup>COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

<sup>3</sup>Subindex access not supported



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**WLG16 A00 BTLE**  
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|--|---|
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Please note the validity of the additional operating instructions for automation functions

ENGLISH

| SICK device specific |              |                 |        |                     |               |  |               |
|----------------------|--------------|-----------------|--------|---------------------|---------------|--|---------------|
| Index dec (hex)      | Name         | Format (Offset) | Length | Access <sup>1</sup> | Default Value | Value / Range  | Remark [Unit] |
| 1086 (0x43E)         | Timer 2 Mode | UInt            | 8 Bit  | rw                  | 0             | 0 = deactivated<br>1 = T-on delay<br>2 = T-off delay<br>3 = T-on/T-off delay<br>4 = Impulse (one shot) |               |
| 1087 (0x43F)         | Time 1 Setup | UInt            | 16 Bit | rw                  | 1             | 1...30000  | [ms]          |
| 1088 (0x440)         | Time 2 Setup | UInt            | 16 Bit | rw                  | 1             | 1...30000  | [ms]          |
| 1089 (0x441)         | Inverter 1   | UInt            | 8 Bit  | rw                  |               | 0 = not inverted<br>1 = inverted   |               |
| 1090 (0x442)         | Inverter 2   | UInt            | 8 Bit  | rw                  |               | 0 = not inverted<br>1 = inverted   |               |

| Standard command |                  |                     |  |           |  |  |               |
|------------------|------------------|---------------------|--|-----------|--|--|---------------|
| Index dec (hex)  |                  | Access <sup>1</sup> |  | Value     | Name   |  | Remark [Unit] |
| 2 (0x02)         | Standard Command | wo                  |  | 65<br>130 | Single value teach<br>Restore Factory Settings |  |               |

| Events         |                      |              |                                      |
|----------------|----------------------|--------------|--------------------------------------|
| Code dec (hex) | Name                 | Type         | Remark [Unit]                        |
| 36000 (0x8CA0) | Short Circuit on Qx  | Notification | Short circuit on outputs             |
| 36001 (0x8CA1) | New parameters       | Notification | Parameters have been changed         |
| 36004 (0x8CA4) | Quality of run alarm | Notification | Low device performance, clean sensor |

| Error          |                 |                                   |   |
|----------------|-----------------|-----------------------------------|---|
| Code dec (hex) | Additional Code | Name                              | Remark [Unit]   |
| 128 (0x80)     | 17 (0x11)       | Index not available               | Access occurs to a not existing index   |
| 128 (0x80)     | 18 (0x12)       | Subindex not available            | Access occurs to a not existing subindex  |
| 128 (0x80)     | 32 (0x20)       | Service temporarily not available | Parameter is not accessible due to the current state of the device application                                |
| 128 (0x80)     | 35 (0x23)       | Access denied                     | Write access on a read-only parameter   |
| 128 (0x80)     | 48 (0x30)       | Parameter value out of range      | Written parameter value is outside its permitted value range  |
| 128 (0x80)     | 51 (0x33)       | Parameter length overrun          | Written parameter length is above its predefined length   |
| 128 (0x80)     | 52 (0x34)       | Parameter length underrun         | Written parameter length is below its predefined length   |
| 128 (0x80)     | 64 (0x40)       | Invalid parameter set             | Written single parameter collides with other actual parameter settings  |
| 128 (0x80)     | 65 (0x41)       | Inconsistent parameter set        | Parameter inconsistencies were found at the end of block parameter transfer, device plausibility check failed |

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<sup>2</sup>COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)  
<sup>3</sup>Subindex access not supported