

# SICK

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## MAS

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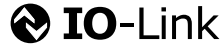
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Please note the validity of the additional operating instructions for automation functions

## 1. Physical layer

Note: The IO-Link Device's max. current consumption (inclusive load current) shall not exceed the master port's max. output power current.

SIO Modus	yes
Min Cycle Time	1.0 ms
Baudrate <sup>2</sup>	COM3
Process Data Length (IN)	4 Byte
IODD version	V2.1
Valid for IO-Link version	1.1.0

## 2. Process data

Record: 4 Byte - Process Data In

Bitoffset																
Byte 0	Angle	31		30		29		28		27		26		25		24
Type/Subindex	Unsigned Integer 16															
Bitoffset																
Byte 1	Angle	23		22		21		20		19		18		17		16
Type/Subindex	Unsigned Integer 16															
Bitoffset																
Byte 2	Direction of Rotation	15		14		13		12		11		10		9		8
Type/Subindex	Boolean	12	Boolean	11	Boolean	10	Boolean	9	Unsigned Integer 2	8	Boolean	7	Magnet Status	6	Magnet Detection	5
Bitoffset																
Byte 3	QL6	7		6		5		4		3		2		1		0
Type/Subindex	Boolean	6	Boolean	5	Boolean	4	Boolean	3	Boolean	2	Boolean	1	QL1	0	Boolean	1

## 3. Service data

The following ISDUs will not be saved via Data-Storage: Teach-In Channel, Rotation speed, Quint15 SP1 / SP2, Quint15 configuration, Quint16 SP1 / SP2, Quint16 configuration, Quint17 SP1 / SP2, Quint17 configuration, Quint18 SP1 / SP2, Quint18 configuration, Quint19 SP1 / SP2, Quint19 configuration, Quint20 SP1 / SP2, Quint20 configuration, Quint21 SP1 / SP2, Quint21 configuration, Quint22 SP1 / SP2, Quint22 configuration, Quint23 SP1 / SP2, Quint23 configuration, Quint24 SP1 / SP2, Quint24 configuration, Quint25 SP1 / SP2, Quint25 configuration, Quint26 SP1 / SP2, Quint26 configuration, Quint27 SP1 / SP2, Quint27 configuration, Quint28 SP1 / SP2, Quint28 configuration, Quint29 SP1 / SP2, Quint29 configuration, Quint30 SP1 / SP2, Quint30 configuration, Quint31 SP1 / SP2, Quint31 configuration, Quint32 SP1 / SP2 and Quint32 configuration

IO-Link specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
12 (0x0C)	Device Access Locks	Record	2 Byte	rw			
1 (0x01)	Parameter (write) Access Lock	Bit (0)	1 Bit	rw			
2 (0x02)	Data Storage Lock	Bit (1)	1 Bit	rw			
3 (0x03)	Local Parameterization Lock	Bit (2)	1 Bit	rw			
4 (0x04)	Local User Interface Lock	Bit (3)	1 Bit	rw			
16 (0x10)	Vendor Name	String	64 Byte	ro	SICK AG		
17 (0x11)	Vendor Text	String	64 Byte	ro	<a href="http://www.sick.com">www.sick.com</a>		
18 (0x12)	Product Name	String	64 Byte	ro	MAS		
19 (0x13)	Product ID	String	64 Byte	ro			
20 (0x14)	Product Text	String	64 Byte	ro			
21 (0x15)	Serial Number	String	16 Byte	ro			
22 (0x16)	Hardware Version	String	64 Byte	ro			
23 (0x17)	Firmware Version	String	64 Byte	ro			
24 (0x18)	Application Specific Tag	String	32 Byte	rw	***		
36 (0x24)	Device Status	UInt	8 Bit	ro		0 = Device is OK 1 = Maintenance required 2 = Out of specification 3 = Functional check 4 = Failure 5...255 = Reserved	
40 (0x28)	Process Data Input	PD In	4 Byte	ro			
SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
13 (0x0D)	Profile Characteristic	Record	10 Byte	ro			
14 (0x0E)	PDInputDescriptor	Record	6 Byte	ro			Process variable input descriptor
15 (0x0F)	PDOOutputDescriptor	Record	3 Byte	ro			Process variable output descriptor

<sup>1</sup> ro = read only, wo = write only, rw = read/write

<sup>2</sup> COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)



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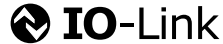
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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
58 (0x3A)	Teach-In Channel	UInt	8 Bit	rw	0	0 = Default Quint (Quint1) 1 = Quint1 2 = Quint2 3 = Quint3 4 = Quint4 5 = Quint5 6 = Quint6 7 = Quint7 8 = Quint8 9 = Quint9 10 = Quint10 11 = Quint11 12 = Quint12 13 = Quint13 14 = Quint14 15 = Quint15 16 = Quint16 17 = Quint17 18 = Quint18 19 = Quint19 20 = Quint20 21 = Quint21 22 = Quint22 23 = Quint23 24 = Quint24 25 = Quint25 26 = Quint26 27 = Quint27 28 = Quint28 29 = Quint29 30 = Quint30 31 = Quint31 32 = Quint32	The Quint channel that is going to be teachd
59 (0x3B)	Teach-In State	Record	1 Byte	ro			Teach-in result provides feedback on the status and the results of the teach-in activities
1 (0x01)	Teach State	Bit (0)	4 Bit	ro	0	0 = IDLE 1 = SP1 SUCCESS 2 = SP2 SUCCESS 3 = SP12 SUCCESS 5 = BUSY 7 = ERROR	Indication of the current state of the teach-in procedure
2 (0x02)	Flag SP TP1	Bit (4)	1 Bit	ro	0		
3 (0x03)	Flag SP TP2	Bit (5)	1 Bit	ro	0	true = no description false = no description	
60 (0x3C)	Quint1 SP1 / SP2	Record	4 Byte	rw			Quint1 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw	0	0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw	12000	0...35999	BDC Setpoint SP2
61 (0x3D)	Quint1 configuration	Record	4 Byte	rw			Quint1 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw	2	0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw	100	0...18000	Switchpoint hysteresis
62 (0x3E)	Quint2 SP1 / SP2	Record	4 Byte	rw			Quint2 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw	12000	0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw	24000	0...35999	BDC Setpoint SP2
63 (0x3F)	Quint2 configuration	Record	4 Byte	rw			Quint2 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw	2	0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw	100	0...18000	Switchpoint hysteresis
64 (0x40)	Device Specific Name	String	32 Byte	rw	***		Device specific name
85 (0x55)	Counting Direction	UInt	8 Bit	rw		0 = Clockwise (cw) 1 = Counterclockwise (ccw)	The counting direction determines at which direction the position value rises.
121 (0x79)	Pin 2 configuration	UInt	8 Bit	rw	81	0 = Deactivated 1 = External input 34 = Switching signal QL2 36 = Detection output Quint2 81 = Home Position Teach-In	
122 (0x7A)	Pin 5 configuration	UInt	8 Bit	rw	90	0 = Deactivated 85 = Teach Feedback Output 90 = Switching signal QL3 94 = Magnet Detection Output 95 = Standstill Output 96 = Rotation Index Pulse Output	
175 (0xAF)	Quality of run	UInt	8 Bit	ro			Quality of magnetic field [%]
204 (0xCC)	Find Me	UInt	8 Bit	wo	0	0 = Stop FindMe 1 = LED flash	
258 (0x102)	Magnet detection	Bool	1 Bit	ro		false = Magnet not detected true = Magnet detected	Magnet detection
263 (0x107)	Pin 4 configuration	UInt	8 Bit	rw	39	35 = Detection output Quint1 39 = Switching signal QL1 85 = Teach Feedback Output 94 = Magnet Detection Output 95 = Standstill Output 	New Variable Description

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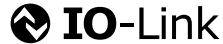
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ENGLISH

SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
1209 (0x4B9)	Input selector 1	Record	5 Byte	rw	1		Bit(0) = Qint1 Bit(31) = Qint32
1210 (0x4BA)	Logic 1	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1211 (0x4BB)	Timer 1 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1212 (0x4BC)	Time 1 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1213 (0x4BD)	Inverter 1	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
1214 (0x4BE)	Input selector 2	Record	5 Byte	rw	2		Bit(0) = Qint1 Bit(31) = Qint32
1215 (0x4BF)	Logic 2	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1216 (0x4C0)	Timer 2 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1217 (0x4C1)	Time 2 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1218 (0x4C2)	Inverter 2	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
1219 (0x4C3)	Input selector 3	Record	5 Byte	rw	4		Bit(0) = Qint1 Bit(31) = Qint32
1220 (0x4C4)	Logic 3	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1221 (0x4C5)	Timer 3 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1222 (0x4C6)	Time 3 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1223 (0x4C7)	Inverter 3	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
1224 (0x4C8)	Input selector 4	Record	5 Byte	rw	0		Bit(0) = Qint1 Bit(31) = Qint32
1225 (0x4C9)	Logic 4	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1226 (0x4CA)	Timer 4 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1227 (0x4CB)	Time 4 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1228 (0x4CC)	Inverter 4	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
1229 (0x4CD)	Input selector 5	Record	5 Byte	rw	0		Bit(0) = Qint1 Bit(31) = Qint32
1230 (0x4CE)	Logic 5	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1231 (0x4CF)	Timer 5 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1232 (0x4D0)	Time 5 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1233 (0x4D1)	Inverter 5	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
1234 (0x4D2)	Input selector 6	Record	5 Byte	rw	0		Bit(0) = Qint1 Bit(31) = Qint32
1235 (0x4D3)	Logic 6	UInt	8 Bit	rw	2	1 = AND 2 = OR	
1236 (0x4D4)	Timer 6 mode	UInt	8 Bit	rw	0	0 = Deactivated 1 = T-on delay 2 = T-off delay 3 = T-on/T-off delay 4 = Impulse (one shot)	
1237 (0x4D5)	Time 6 setup	UInt	16 Bit	rw	1	1...30000	[ms]
1238 (0x4D6)	Inverter 6	UInt	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
4356 (0x1104)	Operating hours [h]	Record	12 Byte	ro			Operating hours data
1 (0x01)	Total	Bit (64)	32 Bit	ro			h
2 (0x02)	Since last reset	Bit (32)	32 Bit	ro			h
3 (0x03)	Since startup	Bit (0)	32 Bit	ro			h
4357 (0x1105)	Power Cycles	Record	12 Byte	ro			Power Cycles data
1 (0x01)	Total	Bit (64)	32 Bit	ro			
2 (0x02)	Since last reset	Bit (32)	32 Bit	ro			
3 (0x03)	Since startup	Bit (0)	32 Bit	ro			

<sup>1</sup> ro = read only, wo = write only, rw = read/write  
<sup>2</sup> COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

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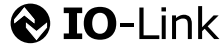
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Please note the validity of the additional operating instructions for automation functions

### ENGLISH

SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
4379 (0x111B)	Rotation Index Pulse Length	UInt	16 Bit	rw	100	1...1000 = Value in ms	New Variable Description [ms]
4380 (0x111C)	Rotation speed	Float	4 Byte	ro			Angular velocity data [rad/s]
4381 (0x111D)	Max Rotation Speed [rad/s]	Record	12 Byte	ro			Max Rotation Speed Value [rad/s]
1 (0x01)	Max. Speed all time	Bit (64)	4 Byte	ro			rad/s
2 (0x02)	Max. Speed since last reset	Bit (32)	4 Byte	ro			rad/s
3 (0x03)	Max. Speed since startup	Bit (0)	4 Byte	ro			rad/s
4383 (0x111F)	Revolution counter	Record	12 Byte	ro			Revolution counter data
1 (0x01)	Total	Bit (64)	32 Bit	ro			
2 (0x02)	Since last reset	Bit (32)	32 Bit	ro			
3 (0x03)	Since startup	Bit (0)	32 Bit	ro			
16380 (0x3FFC)	Angle Offset	Int	16 Bit	rw	0	-18000...18000	Angle Offset [°]
16384 (0x4000)	Qint3 SP1 / SP2	Record	4 Byte	rw			Qint3 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw	24000	0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw	35999	0...35999	BDC Setpoint SP2
16385 (0x4001)	Qint3 configuration	Record	4 Byte	rw			Qint3 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw	2	0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw	100	0...18000	Switchpoint hysteresis
16386 (0x4002)	Qint4 SP1 / SP2	Record	4 Byte	rw			Qint4 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16387 (0x4003)	Qint4 configuration	Record	4 Byte	rw			Qint4 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16388 (0x4004)	Qint5 SP1 / SP2	Record	4 Byte	rw			Qint5 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16389 (0x4005)	Qint5 configuration	Record	4 Byte	rw			Qint5 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16390 (0x4006)	Qint6 SP1 / SP2	Record	4 Byte	rw			Qint6 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16391 (0x4007)	Qint6 configuration	Record	4 Byte	rw			Qint6 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16392 (0x4008)	Qint7 SP1 / SP2	Record	4 Byte	rw			Qint7 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16393 (0x4009)	Qint7 configuration	Record	4 Byte	rw			Qint7 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16394 (0x400A)	Qint8 SP1 / SP2	Record	4 Byte	rw			Qint8 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16395 (0x400B)	Qint8 configuration	Record	4 Byte	rw			Qint8 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16396 (0x400C)	Qint9 SP1 / SP2	Record	4 Byte	rw			Qint9 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16397 (0x400D)	Qint9 configuration	Record	4 Byte	rw			Qint9 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16398 (0x400E)	Qint10 SP1 / SP2	Record	4 Byte	rw			Qint10 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16399 (0x400F)	Qint10 configuration	Record	4 Byte	rw			Qint10 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16400 (0x4010)	Qint11 SP1 / SP2	Record	4 Byte	rw			Qint11 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16401 (0x4011)	Qint11 configuration	Record	4 Byte	rw			Qint11 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16402 (0x4012)	Qint12 SP1 / SP2	Record	4 Byte	rw			Qint12 Setpoints

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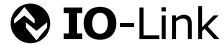
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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access <sup>1</sup>	Default Value	Value / Range	Remark [Unit]
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16403 (0x4013)	Qint12 configuration	Record	4 Byte	rw			Qint12 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16404 (0x4014)	Qint13 SP1 / SP2	Record	4 Byte	rw			Qint13 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16405 (0x4015)	Qint13 configuration	Record	4 Byte	rw			Qint13 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16406 (0x4016)	Qint14 SP1 / SP2	Record	4 Byte	rw			Qint14 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16407 (0x4017)	Qint14 configuration	Record	4 Byte	rw			Qint14 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16408 (0x4018)	Qint15 SP1 / SP2	Record	4 Byte	rw			Qint15 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16409 (0x4019)	Qint15 configuration	Record	4 Byte	rw			Qint15 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16410 (0x401A)	Qint16 SP1 / SP2	Record	4 Byte	rw			Qint16 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16411 (0x401B)	Qint16 configuration	Record	4 Byte	rw			Qint16 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16412 (0x401C)	Qint17 SP1 / SP2	Record	4 Byte	rw			Qint17 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16413 (0x401D)	Qint17 configuration	Record	4 Byte	rw			Qint17 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16414 (0x401E)	Qint18 SP1 / SP2	Record	4 Byte	rw			Qint18 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16415 (0x401F)	Qint18 configuration	Record	4 Byte	rw			Qint18 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16416 (0x4020)	Qint19 SP1 / SP2	Record	4 Byte	rw			Qint19 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16417 (0x4021)	Qint19 configuration	Record	4 Byte	rw			Qint19 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16418 (0x4022)	Qint20 SP1 / SP2	Record	4 Byte	rw			Qint20 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16419 (0x4023)	Qint20 configuration	Record	4 Byte	rw			Qint20 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16420 (0x4024)	Qint21 SP1 / SP2	Record	4 Byte	rw			Qint21 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16421 (0x4025)	Qint21 configuration	Record	4 Byte	rw			Qint21 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16422 (0x4026)	Qint22 SP1 / SP2	Record	4 Byte	rw			Qint22 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1
2 (0x02)	Setpoint SP2	Bit (0)	16 Bit	rw		0...35999	BDC Setpoint SP2
16423 (0x4027)	Qint22 configuration	Record	4 Byte	rw			Qint22 configuration
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw		0 = Deactivated 2 = Window mode	Switchpoint mode
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw		0...18000	Switchpoint hysteresis
16424 (0x4028)	Qint23 SP1 / SP2	Record	4 Byte	rw			Qint23 Setpoints
1 (0x01)	Setpoint SP1	Bit (16)	16 Bit	rw		0...35999	BDC Setpoint SP1

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