

SICK

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MPS-G
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9326418 18KF

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SICK AG, Erwin-Sick-Strasse 1, D.79183 Waldkirch

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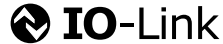
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Please note the validity of the additional operating instructions for automation functions

ENGLISH

1. Physical layer

Note: The IO-Link Device's max. current consumption (inclusive load current) shall not exceed the master port's max. output power current.

SIO Modus	yes
Min Cycle Time	1.0 ms
Baudrate ²	COM3
Process Data Length (IN)	4 Byte
IODD version	V1.02
Valid for IO-Link version	1.1.0

2. Process data

Record: 4 Byte

Bitoffset																	
Byte 0	Position	31	30	29	28	27	26	25	24								
Type/Subindex	Integer 16																
Bitoffset																	
Byte 1	Position	23	22	21	20	19	18	17	16								
Type/Subindex	Integer 16																
Bitoffset																	
Byte 2	Quint.16	15	14	13	12	11	10	9	8	Quint.15	14	13	12	11	10	9	8
Type/Subindex	Boolean	16	Boolean	15	Boolean	14	Boolean	13	Boolean	12	Boolean	11	Boolean	10	Boolean	9	Boolean
Bitoffset																	
Byte 3	Quint.8	7	6	5	4	3	2	1	0	Quint.7	6	5	4	3	2	1	0
Type/Subindex	Boolean	8	Boolean	7	Boolean	6	Boolean	5	Boolean	4	Boolean	3	Boolean	2	Boolean	1	Boolean

3. Service data

The following ISDUs will not be saved via Data-Storage: Teach-in Channel and Device Specific Name

IO-Link specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
12 (0x0C)	Device Access Locks	Record	2 Byte	rw			
1 (0x01)	Parameter (write) Access Lock	Bit (0)	1 Bit	rw			
2 (0x02)	Data Storage Lock	Bit (1)	1 Bit	rw			
3 (0x03)	Local Parameterization Lock	Bit (2)	1 Bit	rw			
4 (0x04)	Local User Interface Lock	Bit (3)	1 Bit	rw			
16 (0x10)	Vendor Name	String	64 Byte	ro	SICK AG		
17 (0x11)	Vendor Text	String	64 Byte	ro	www.sick.com		
18 (0x12)	Product Name	String	64 Byte	ro	MPS-G		
19 (0x13)	Product ID	String	64 Byte	ro			
20 (0x14)	Product Text	String	64 Byte	ro			
21 (0x15)	Serial Number	String	16 Byte	ro			
22 (0x16)	Hardware Version	String	64 Byte	ro			
23 (0x17)	Firmware Version	String	64 Byte	ro			
24 (0x18)	Application Specific Tag	String	32 Byte	rw	***		
36 (0x24)	Device Status	UInt	8 Bit	ro		0 = Device is OK 1 = Maintenance required 2 = Out of specification 3 = Functional check 4 = Failure 5...255 = Reserved	
40 (0x28)	Process Data Input	PD In	4 Byte	ro			
SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
13 (0x0D)	Profile Characteristic	Record	10 Byte	ro			
1 (0x01)	Profile Identifier	Bit (64)	16 Bit	ro	1		
2 (0x02)	Profile Identifier	Bit (48)	16 Bit	ro	32768		
3 (0x03)	Profile Identifier	Bit (32)	16 Bit	ro	32769		

¹ ro = read only, wo = write only, rw = read/write

² COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

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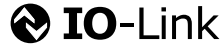
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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
4 (0x04)	Profile Identifier	Bit (16)	16 Bit	ro	32770		
5 (0x05)	Profile Identifier	Bit (0)	16 Bit	ro	32772		
14 (0x0E)	PDInputDescriptor	Record	6 Byte	ro	PDInputDescriptor		
15 (0x0F)	PDOOutputDescriptor	Record	3 Byte	ro	PDOOutputDescriptor		
58 (0x3A)	Teach-in Channel	UInt	8 Bit	rw	0	0 = Default Quint (Quint.1) 1 = Quint.1 2 = Quint.2 3 = Quint.3 4 = Quint.4 5 = Quint.5 6 = Quint.6 7 = Quint.7 8 = Quint.8 9 = Quint.9 10 = Quint.10 11 = Quint.11 12 = Quint.12 13 = Quint.13 14 = Quint.14 15 = Quint.15 16 = Quint.16	The Quint channel that is going to be teachd
59 (0x3B)	Teach-in State	Record	1 Byte	ro	Teach-in result provides feedback on the status and the results of the teach-in activities		
1 (0x01)	State	Bit (0)	4 Bit	ro	0	0 = IDLE 1 = SP1 SUCCESS 2 = SP2 SUCCESS 3 = SP12 SUCCESS 4 = WAIT FOR COMMAND 5 = BUSY 7 = ERROR	
2 (0x02)	Flag SP1 TP1	Bit (4)	1 Bit	ro	false	true false	
3 (0x03)	Flag SP1 TP2	Bit (7)	1 Bit	ro	false	true false	
4 (0x04)	Flag SP2 TP1	Bit (6)	1 Bit	ro	false	true false	
5 (0x05)	Flag SP2 TP2	Bit (5)	1 Bit	ro	false	true false	
60 (0x3C)	Quint.1 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
61 (0x3D)	Quint.1 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 130 = Move 131 = Grip	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
62 (0x3E)	Quint.2 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
63 (0x3F)	Quint.2 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 130 = Move 131 = Grip	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
64 (0x40)	Device Specific Name	String	32 Byte	rw	***		
120 (0x78)	Process Data Select	UInt	8 Bit	rw	0	0 = Position (2 Bytes) + 16xBDC (2 Bytes)	
121 (0x79)	Pin 2 Configuration	UInt	8 Bit	rw	0	0 = Deactivated 36 = Quint.2	

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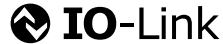
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Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
170 (0xAA)	Switchpoint Width [x10µm]	Record	32 Byte	rw		Switchpoint width	
1 (0x01)	Qint.1 Width	Bit (240)	16 Bit	rw	200	0...1000	
2 (0x02)	Qint.2 Width	Bit (224)	16 Bit	rw	200	0...1000	
3 (0x03)	Qint.3 Width	Bit (208)	16 Bit	rw	200	0...1000	
4 (0x04)	Qint.4 Width	Bit (192)	16 Bit	rw	200	0...1000	
5 (0x05)	Qint.5 Width	Bit (176)	16 Bit	rw	200	0...1000	
6 (0x06)	Qint.6 Width	Bit (160)	16 Bit	rw	200	0...1000	
7 (0x07)	Qint.7 Width	Bit (144)	16 Bit	rw	200	0...1000	
8 (0x08)	Qint.8 Width	Bit (128)	16 Bit	rw	200	0...1000	
9 (0x09)	Qint.9 Width	Bit (112)	16 Bit	rw	200	0...1000	
10 (0x0A)	Qint.10 Width	Bit (96)	16 Bit	rw	200	0...1000	
11 (0x0B)	Qint.11 Width	Bit (80)	16 Bit	rw	200	0...1000	
12 (0x0C)	Qint.12 Width	Bit (64)	16 Bit	rw	200	0...1000	
13 (0x0D)	Qint.13 Width	Bit (48)	16 Bit	rw	200	0...1000	
14 (0x0E)	Qint.14 Width	Bit (32)	16 Bit	rw	200	0...1000	
15 (0x0F)	Qint.15 Width	Bit (16)	16 Bit	rw	200	0...1000	
16 (0x10)	Qint.16 Width	Bit (0)	16 Bit	rw	200	0...1000	
171 (0xAB)	Switchpoint Tolerance [x10µm]	Record	6 Byte	rw		Switchpoint tolerance	
1 (0x01)	Qint.1 Tolerance	Bit (32)	16 Bit	rw	100		
2 (0x02)	Qint.2 Tolerance	Bit (16)	16 Bit	rw	100		
3 (0x03)	Qint.3 Tolerance	Bit (0)	16 Bit	rw	100		
204 (0xCC)	Find Me	UInt	8 Bit	wo	0		0 = Stop FindMe 1 = LED flash
256 (0x100)	Position [x10µm]	Int	16 Bit	ro		Current position [x10µm]	
257 (0x101)	Position Offset [x10µm]	Int	16 Bit	rw	0		Offset that is added to the current position [x10µm]
261 (0x105)	Current Position Repeatability [mm]	Float	4 Byte	ro			
4372 (0x1114)	Actuator Travel [x10µm]	UInt	16 Bit	ro			
4374 (0x1116)	Total Actuator Travel [sum m]	Float	4 Byte	rw			
4375 (0x1117)	Average Actuator Velocity [m/s]	Record	8 Byte	ro			
1 (0x01)	Extend (positive direction)	Bit (32)	4 Byte	ro			
2 (0x02)	Retract (negative direction)	Bit (0)	4 Byte	ro			
4379 (0x111B)	Actuator Travel Time [ms]	Record	8 Byte	ro			
1 (0x01)	Extend (positive direction)	Bit (32)	4 Byte	ro			
2 (0x02)	Retract (negative direction)	Bit (0)	4 Byte	ro			
4380 (0x111C)	Cycle Time [ms]	Float	4 Byte	ro			
4381 (0x111D)	Dwell Time [ms]	Record	8 Byte	ro			
1 (0x01)	Start Position	Bit (32)	4 Byte	ro			
2 (0x02)	Stop Position	Bit (0)	4 Byte	ro			
4382 (0x111E)	Cycle Count [sum]	UInt	32 Bit	rw			
4602 (0x11FA)	Current Field Strength [mT]	Record	8 Byte	ro			
1 (0x01)	Current1	Bit (32)	4 Byte	ro		Magnetic field strength of TMR 1	
2 (0x02)	Current2	Bit (0)	4 Byte	ro		Magnetic field strength of TMR2	
4604 (0x11FC)	Peak Field Strength [mT]	Record	8 Byte	ro			
1 (0x01)	Current1	Bit (32)	4 Byte	ro		Magnetic field strength of TMR 1	
2 (0x02)	Current2	Bit (0)	4 Byte	ro		Magnetic field strength of TMR2	
14905 (0x3A39)	Platform Version	String	32 Byte	ro		Platform Version	
14908 (0x3A3C)	Sensor Head Firmware Version	String	32 Byte	ro		Sensor Head Firmware Version	

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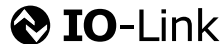
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Please note the validity of the additional operating instructions for automation functions

ENGLISH

SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
16384 (0x4000)	Qint.3 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
	2 (0x02) SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16385 (0x4001)	Qint.3 Configuration	Record	4 Byte	rw			
	1 (0x01) Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode 131 = Grip	
	3 (0x03) Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16386 (0x4002)	Qint.4 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
	2 (0x02) SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16387 (0x4003)	Qint.4 Configuration	Record	4 Byte	rw			
	1 (0x01) Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
	3 (0x03) Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16388 (0x4004)	Qint.5 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
	2 (0x02) SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16389 (0x4005)	Qint.5 Configuration	Record	4 Byte	rw			
	1 (0x01) Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
	3 (0x03) Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16390 (0x4006)	Qint.6 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
	2 (0x02) SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16391 (0x4007)	Qint.6 Configuration	Record	4 Byte	rw			
	1 (0x01) Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
	3 (0x03) Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16392 (0x4008)	Qint.7 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
	2 (0x02) SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16393 (0x4009)	Qint.7 Configuration	Record	4 Byte	rw			
	1 (0x01) Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
	3 (0x03) Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16394 (0x400A)	Qint.8 SP1 / SP2	Record	4 Byte	rw			
	1 (0x01) SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	

¹ ro = read only, wo = write only, rw = read/write

² COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)



8025819 0720
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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16406 (0x4016)	Qint.14 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16407 (0x4017)	Qint.14 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16408 (0x4018)	Qint.15 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16409 (0x4019)	Qint.15 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
16410 (0x401A)	Qint.16 SP1 / SP2	Record	4 Byte	rw			
1 (0x01)	SP1	Bit (16)	16 Bit	rw	0	Setpoint 1	
2 (0x02)	SP2	Bit (0)	16 Bit	rw	0	Setpoint 2	
16411 (0x401B)	Qint.16 Configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint Logic	Bit (24)	8 Bit	rw	0	0 = Not inverted 1 = Inverted	
2 (0x02)	Switchpoint Mode	Bit (16)	8 Bit	rw	0	0 = Deactivated 1 = Single Point Mode 2 = Window Mode 3 = Two Point Mode 129 = Cylinder Switch Mode	
3 (0x03)	Switchpoint Hysteresis	Bit (0)	16 Bit	rw	70		
Standard command							
Index dec (hex)	Standard Command	Access ¹	Value	Name	Remark [Unit]		
2 (0x02)	Standard Command	wo	65	SP1 Single Value Teach			
			66	SP2 Single Value Teach			
			75	Dynamic Teach Start			
			76	Dynamic Teach Stop			
			77	Dynamic Teach Apply			
			79	Teach Cancel			
			128	Device Reset			
			130	Restore Factory Settings			

¹ ro = read only, wo = write only, rw = read/write

² COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)